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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO et al.**

Group Art Unit: **2814**

Serial No.: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation Number: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE  
THRESHOLD CHARACTERISTIC**

Attorney Docket Number: **020244**

Customer Number: **38834**

**REQUEST FOR RECONSIDERATION AFTER FINAL REJECTION**

Mail Stop: AF

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

September 13, 2005

Sir:

This paper is filed in response to the Office Action dated June 15, 2005.

Remarks begin on page 2 of this paper.

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